

Application No. 10/777,990  
Response dated: December 16, 2005  
Reply to Final Office action dated: October 17, 2005

**Amendments to the Claims:**

This listing of claims will replace all prior versions, and listing, of claims in the application.

**Listing of Claims:**

1. – 9. (Cancelled)

10. (Currently Amended) A substrate for a display panel, comprising:

a lower substrate having an inspection line receiving a first inspection signal externally provided, a driving part formed at the lower substrate and outputting a second inspection signal in response to the first inspection signal provided through the inspection line, and a pixel part being driven in response to the second inspection signal; and

an upper substrate being coupled to the lower substrate;

~~wherein the driving part has a plurality of first switching devices formed on the lower substrate, and the pixel part has a plurality of second switching devices formed on the lower substrate.~~

11. (Original) The substrate of claim 10, wherein the inspection line comprises:

a plurality of input lines spaced apart from each other in a predetermined distance; and  
a connecting line electrically connected between the input lines.

12. (Currently Amended) The substrate of claim 11, wherein ~~an~~ end portions of the

input lines are disposed on an edge portion of the lower substrate, and

a connecting line electrically connected between the end portions of the input lines is disposed on the edge portion of the lower substrate.

13. (Previously Presented) The substrate of claim 12, wherein the lower substrate is

partially grinded, the end portions of the input lines disposed on the edge portion and a portion of the connecting line disposed on the edge portion are removed while the lower substrate is grinded.

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14. (Previously Presented) The substrate of claim 11, wherein the input lines comprise a start signal input line, a clock input line and a driving voltage input line.

15. (Original) The substrate of claim 14, wherein the driving voltage input line has a width wider than those of the start signal input line and clock input line.

16. (Original) The substrate of claim 15, wherein the driving voltage input line receives a first inspection signal externally provided, and provides the first inspection signal to the inspection line.

17. (Original) The substrate of claim 10, further comprising a liquid crystal layer disposed between the lower substrate and the upper substrate.

18. (Currently Amended) A method of manufacturing a display panel, comprising:  
fabricating a substrate for a display panel, the substrate having a lower substrate and an upper substrate coupled to the lower substrate, the lower substrate having an inspection line receiving a first inspection signal externally provided, a driving part formed at the lower substrate outputting a second inspection signal in response to the first inspection signal provided through the inspection line, and a pixel part being driven in response to the second inspection signal;

providing the first inspection signal to the inspection line to inspect the driving part and pixel part; and

insulating the inspection line from an input line to complete the display panel;  
~~wherein the driving part and the pixel part are formed on the lower substrate.~~

19. (Original) The method of claim 18, further comprising:  
fabricating a mother substrate for the lower substrate having an inspecting pad part extended from the inspection line;

providing the first inspection signal to the inspecting pad part to inspect the mother substrate for the lower substrate;

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fabricating a mother substrate for the upper substrate;  
combining the mother substrate for the lower substrate with the mother substrate for the upper substrate; and  
cutting the combined substrate to complete the substrate for the display panel.

20. (Original) The method of claim 18, wherein the inspection line is removed by grinding an edge of the substrate for the display panel.

21. – 23. (Cancelled)